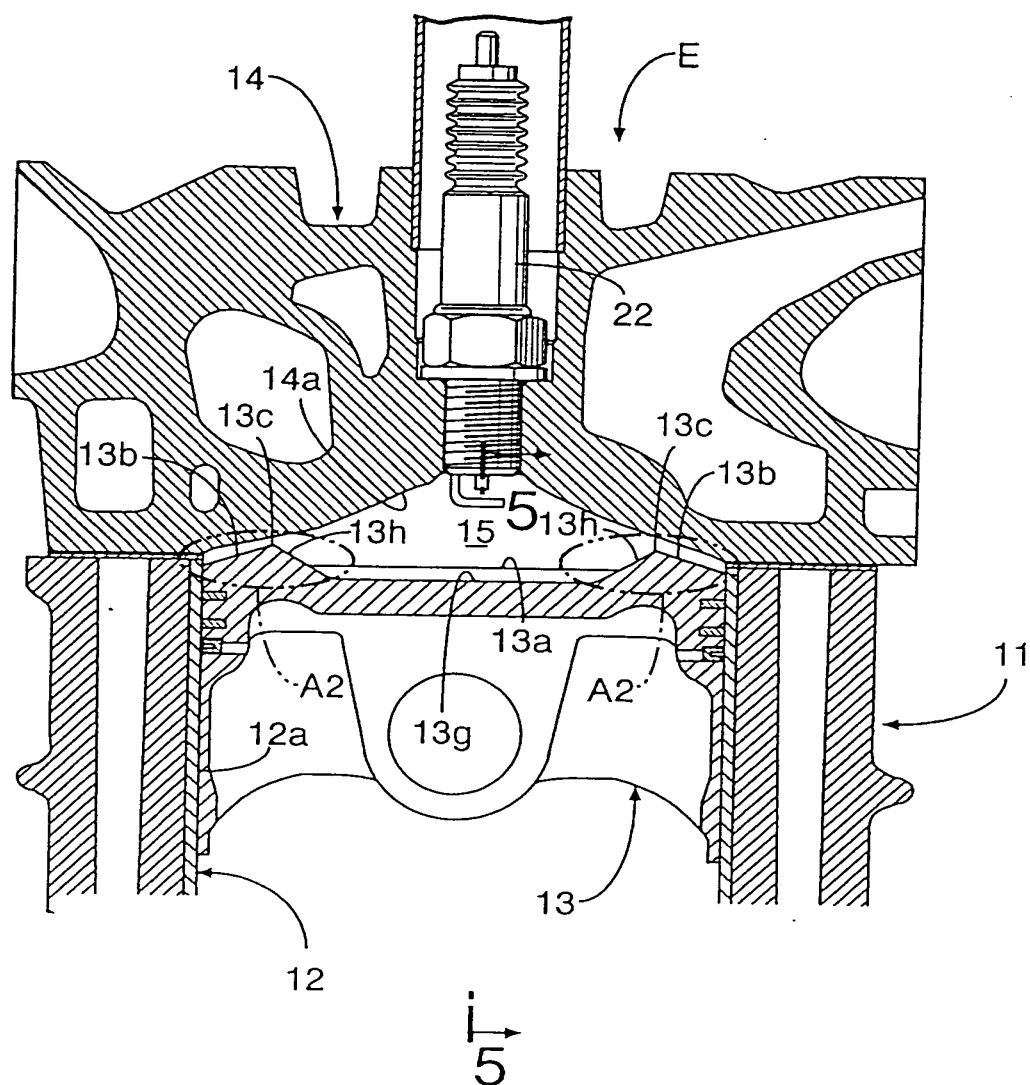
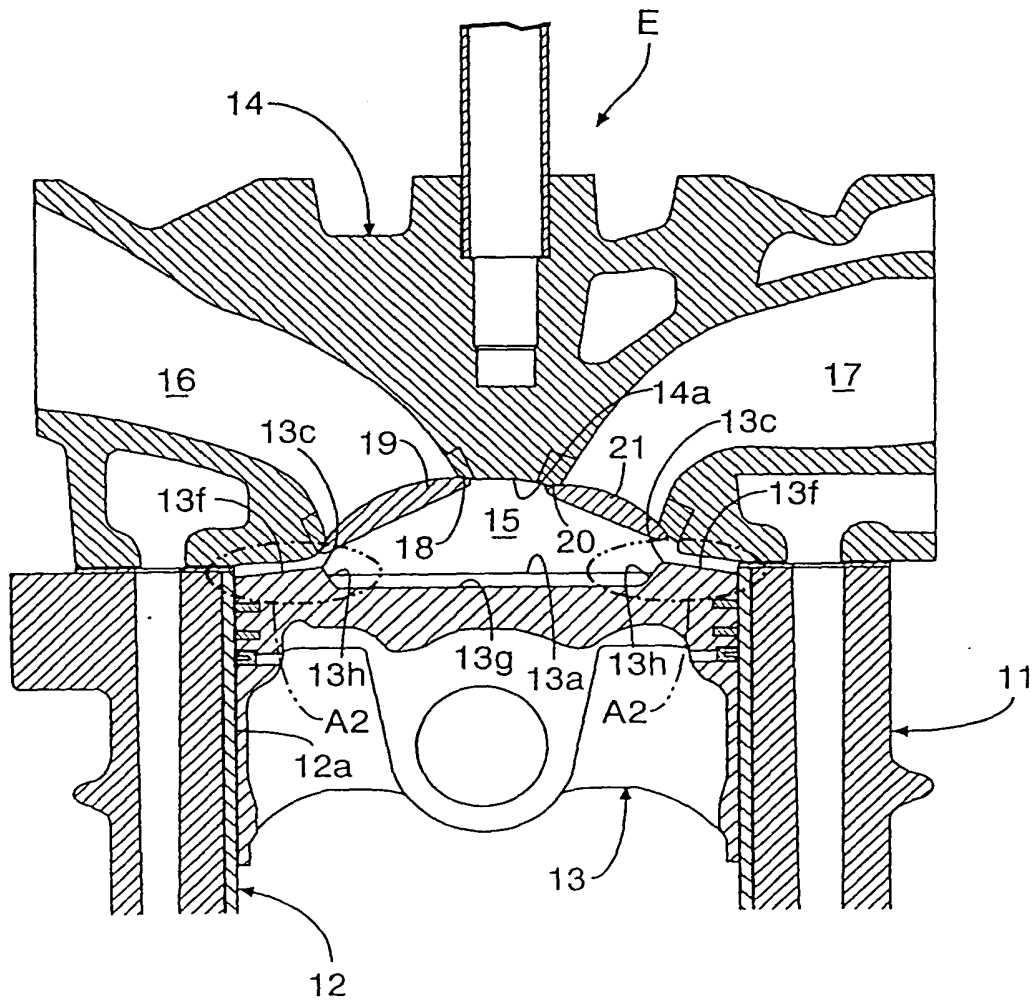


Fig. 1



—[3]—

Fig. 2



—[図4]—

Fig. 3

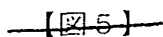
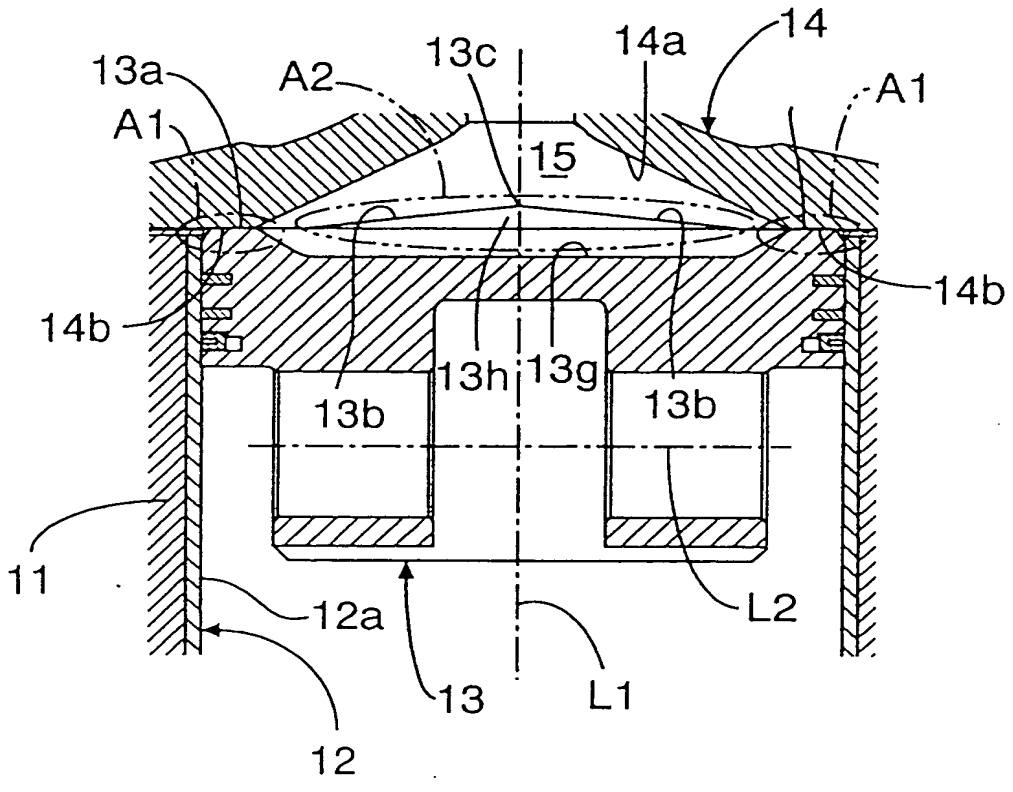
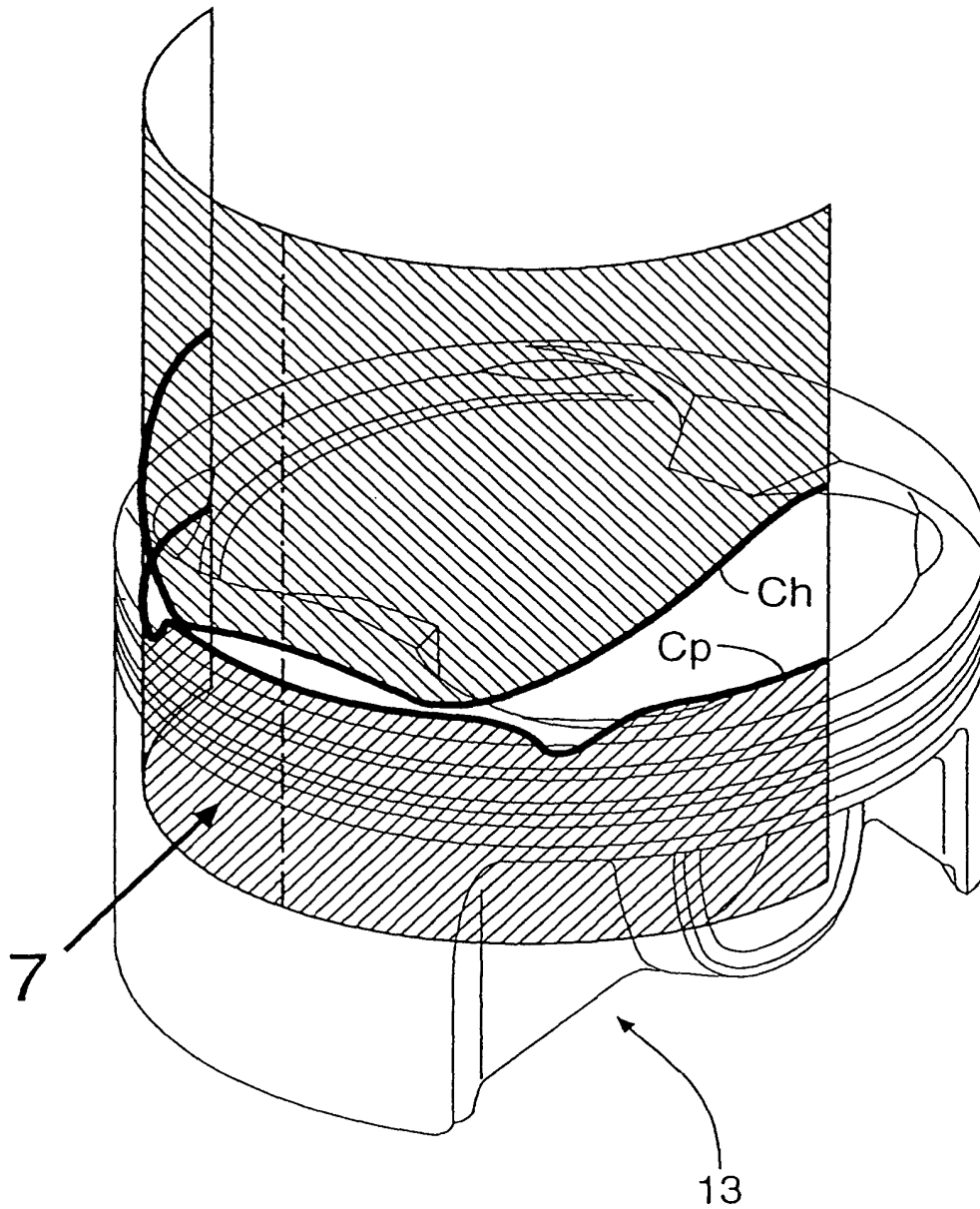


Fig. 4



—[6]—

Fig. 5



—【図 7】—

Fig. 6

Figure 13 is a cross-sectional diagram of a semiconductor device. It shows a substrate 13a with a layer 13b. A dashed line A2 represents a reference plane. The diagram illustrates the profile of a layer 13f and its thickness variations, labeled with angles α and β , and points 13d, 13b, 13a, 13f, 14a, Ch, and Cp.